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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10008696	FILING DATE 11/08/2001	CLASS 712 711 710	SUBCLASS 29 240	GAH 2105	EXAMINER Richard K Dang
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2184
2/8/11

****CONTINUING DATA VERIFIED:**
THIS APPLN CLAIMS BENEFIT OF G0/249,083 11/15/2000

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**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB <input type="checkbox"/>	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claims: 35 USC 119 conditions met Verified and Acknowledged Examiners's initials	<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO TI-30105
TITLE : Multicore DSP device having coupled subsystem memory buses for global DMA access <small>U.S. DEPT. OF COMM./PAT. & TM-PTO-435L (Rev. 12-94)</small>		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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